

This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
16 August 2001 (16.08.2001)

PCT

(10) International Publication Number  
WO 01/59443 A1

(51) International Patent Classification<sup>7</sup>: G01N 27/416

(21) International Application Number: PCT/US01/04193

(22) International Filing Date: 9 February 2001 (09.02.2001)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:

60/181,854	11 February 2000 (11.02.2000)	US
09/544,696	7 April 2000 (07.04.2000)	US
60/204,345	15 May 2000 (15.05.2000)	US
60/218,878	18 July 2000 (18.07.2000)	US
60/224,092	9 August 2000 (09.08.2000)	US

(71) Applicant: MIDTRONICS, INC. [US/US]; 7000 Monroe Street, Willowbrook, IL 60521 (US).

(74) Agents: CHAMPLIN, Judson, K. et al.; Westman, Champlin & Kelly, P.A., Suite 1600 - International Centre, 900 Second Avenue South, Minneapolis, MN 55402-3319 (US).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

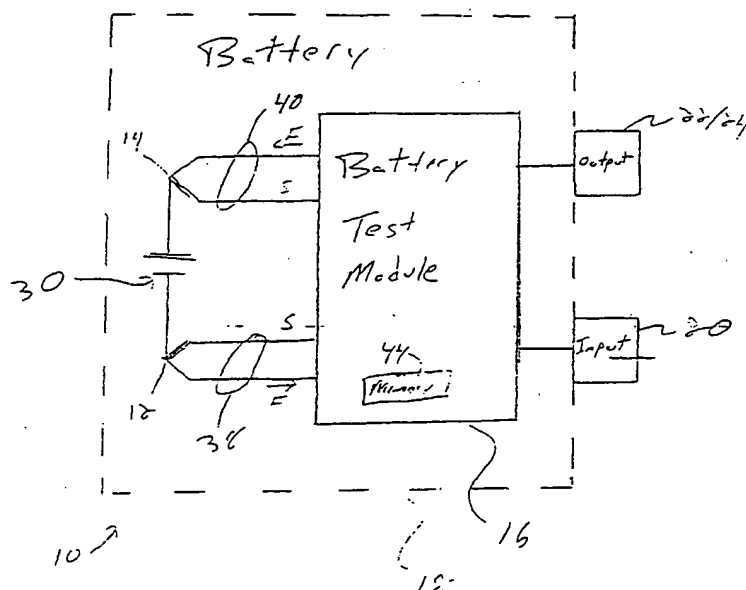
(72) Inventors: BERTNESS, Kevin, I.; 1317 McClurg Drive, Batavia, IL 60510 (US). BUTTERIS, Jamey; 7912 Woodlyn Drive, Apt. 103, Woodridge, IL 60517 (US).

Published:

— with international search report

[Continued on next page]

(54) Title: STORAGE BATTERY WITH INTEGRAL BATTERY TESTER



(57) Abstract: A storage battery (10) includes a battery housing (18) and a plurality of electrochemical cells (30) in the battery housing electrically connected in series to a positive terminal (12) of the battery and a negative terminal (14) of the battery. A first connection (38) is coupled to the positive terminal of the battery and a second connection (40) is coupled to the negative terminal of the battery. A battery test module is mounted to the battery housing and electrically coupled to the positive and negative terminals through the respective first and second Kelvin connections. A display or other output is configured to output a battery test result from the battery test module.

WO 01/59443 A1



*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

## STORAGE BATTERY WITH INTEGRAL BATTERY TESTER

### BACKGROUND OF THE INVENTION

The present invention relates to storage  
5 batteries. More specifically, the present invention  
relates to storage batteries with integral battery  
testers.

Storage batteries, such as lead acid storage  
batteries, are used in a variety of applications such  
10 as automotive vehicles and standby power sources.  
Typical storage batteries consist of a plurality of  
individual storage cells which are electrically  
connected in series. Each cell can have a voltage  
potential of about 2.1 volts, for example. By  
15 connecting the cells in the series, the voltages of  
the individual cells are added in a cumulative manner.  
For example, in a typical automotive storage battery,  
six storage cells are used to provide a total voltage  
of about 12.6 volts. The individual cells are held in  
20 a housing and the entire assembly is commonly referred  
to as the "battery."

It is frequently desirable to ascertain the  
condition of a storage battery. Various testing  
techniques have been developed over the long history  
25 of storage batteries. For example, one technique  
involves the use of a hygrometer in which the specific  
gravity of the acid mixture in the battery is  
measured. Electrical testing has also been used to  
provide less invasive battery testing techniques. A  
30 very simple electrical test is to simply measure the  
voltage across the battery. If the voltage is below a  
certain threshold, the battery is determined to be  
bad. Another technique for testing a battery is

-2-

referred to as a load test. In a load test, the battery is discharged using a known load. As the battery is discharged, the voltage across the battery is monitored and used to determine the condition of the battery. More recently, a technique has been pioneered by Dr. Keith S. Champlin and Midtronics, Inc. of Willowbrook, Illinois for testing storage battery by measuring a dynamic parameter of the battery such as the dynamic conductance of the battery. This technique is described in a number of United States patents, for example, U.S. Patent No. 3,873,911, issued March 25, 1975, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 3,909,708, issued September 30, 1975, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,816,768, issued March 28, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,825,170, issued April 25, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH AUTOMATIC VOLTAGE SCALING; U.S. Patent No. 4,881,038, issued November 14, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH AUTOMATIC VOLTAGE SCALING TO DETERMINE DYNAMIC CONDUCTANCE; U.S. Patent No. 4,912,416, issued March 27, 1990, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH STATE-OF-CHARGE COMPENSATION; U.S. Patent No. 5,140,269, issued August 18, 1992, to Champlin, entitled ELECTRONIC TESTER FOR ASSESSING BATTERY/CELL CAPACITY; U.S. Patent No. 5,343,380, issued August 30, 1994, entitled METHOD AND APPARATUS FOR SUPPRESSING TIME VARYING SIGNALS IN BATTERIES UNDERGOING CHARGING OR DISCHARGING; U.S. Patent No. 5,572,136, issued November 5, 1996, entitled

-3-

ELECTRONIC BATTERY TESTER WITH AUTOMATIC COMPENSATION  
FOR LOW STATE-OF-CHARGE; U.S. Patent No. 5,574,355,  
issued November 12, 1996, entitled METHOD AND  
APPARATUS FOR DETECTION AND CONTROL OF THERMAL RUNAWAY  
5 IN A BATTERY UNDER CHARGE; U.S. Patent No. 5,585,728,  
issued December 17, 1996, entitled ELECTRONIC BATTERY  
TESTER WITH AUTOMATIC COMPENSATION FOR LOW STATE-OF-  
CHARGE; U.S. Patent No. 5,592,093, issued January 7,  
1997, entitled ELECTRONIC BATTERY TESTING DEVICE LOOSE  
10 TERMINAL CONNECTION DETECTION VIA A COMPARISON  
CIRCUIT; U.S. Patent No. 5,598,098, issued January 28,  
1997, entitled ELECTRONIC BATTERY TESTER WITH VERY  
HIGH NOISE IMMUNITY; U.S. Patent No. 5,757,192, issued  
May 26, 1998, entitled METHOD AND APPARATUS FOR  
15 DETECTING A BAD CELL IN A STORAGE BATTERY; U.S. Patent  
No. 5,821,756, issued October 13, 1998, entitled  
ELECTRONIC BATTERY TESTER WITH TAILORED COMPENSATION  
FOR LOW STATE-OF-CHARGE; U.S. Patent No. 5,831,435,  
issued November 3, 1998, entitled BATTERY TESTER FOR  
20 JIS STANDARD; U.S. Patent No. 5,914,605, issued June  
22, 1999, entitled ELECTRONIC BATTERY TESTER; U.S.  
Patent No. 5,945,829, issued August 31, 1999, entitled  
MIDPOINT BATTERY MONITORING; U.S. Patent No.  
6,002,238, issued December 14, 1999, entitled METHOD  
25 AND APPARATUS FOR MEASURING COMPLEX IMPEDANCE OF CELLS  
AND BATTERIES; U.S. Patent No. 6,037,777, issued March  
14, 2000, entitled METHOD AND APPARATUS FOR  
DETERMINING BATTERY PROPERTIES FROM COMPLEX  
IMPEDANCE/ADMITTANCE; U.S. Patent No. 6,051,976,  
30 issued April 18, 2000, entitled METHOD AND APPARATUS  
FOR AUDITING A BATTERY TEST; U.S. Patent No.  
6,081,098, issued June 27, 2000, entitled METHOD AND  
APPARATUS FOR CHARGING A BATTERY; U.S. Patent No.

-4-

6,091,245, issued July 18, 2000, entitled METHOD AND APPARATUS FOR AUDITING A BATTERY TEST; U.S. Patent No. 6,104,167, issued August 15, 2000, entitled METHOD AND APPARATUS FOR CHARGING A BATTERY; U.S. Patent No. 5 6,137,269, issued October 24, 2000, entitled METHOD AND APPARATUS FOR ELECTRONICALLY EVALUATING THE INTERNAL TEMPERATURE OF AN ELECTROCHEMICAL CELL OR BATTERY; U.S. Patent No. 6,163,156, issued December 19, 2000, entitled ELECTRICAL CONNECTION FOR 10 ELECTRONIC BATTERY TESTER; U.S. Patent No. 6,172,483, issued January 9, 2001, entitled METHOD AND APPARATUS FOR MEASURING COMPLEX IMPEDANCE OF CELL AND BATTERIES; and U.S. Patent No. 6,172,505, issued January 9, 2001, entitled ELECTRONIC BATTERY TESTER.

15 In general, battery testers have been separate pieces of equipment which can be moved between storage batteries and electrically coupled to a storage battery. The prior art has lacked a simple technique for the testing of a storage battery without 20 relying on separate testing equipment.

#### SUMMARY OF THE INVENTION

A storage battery includes a battery housing and a plurality of electrochemical cells in the battery housing electrically connected in series to a 25 positive terminal of the battery and a negative terminal of the battery. A first connection is coupled to the positive terminal of the battery and a second connection is coupled to the negative terminal of the battery. A battery test module is mounted to the 30 battery housing and electrically coupled to the positive and negative terminals through the respective first and second Kelvin connection. A display or other

-5-

output is configured to output a battery test result from the battery test module.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a side plan view of a storage battery including a battery test module in accordance with the present invention.

Figures 2A and 2B are top plan views of the storage battery of Figure 1.

Figure 3 is a side cross-sectional view of the storage battery of Figures 1 and 2 taken along the line labeled 3--3 in Figure 2.

Figure 4 is a block diagram of a storage battery in accordance with the present invention.

Figure 5 is an electrical diagram of one example embodiment.

Figure 6 is an electrical diagram of another example embodiment.

#### DETAILED DESCRIPTION OF THE ILLUSTRATIVE EMBODIMENTS

In one aspect of the present invention a storage battery is provided having an integrated battery test module for performing a battery test on electrical cells of the storage battery. As used herein "integrated" can include a separate module which is attached to the battery housing. In one embodiment, the battery test module is electrically coupled to the electrical cells of the storage battery through Kelvin connections. In certain aspects, Kelvin connections are not used. As the battery test module is integral with the battery, an operator can test the battery without relying on external battery test equipment. In one embodiment, the battery test is one that can be easily performed by an unskilled operator. Further, the battery test module is preferably

-6-

manufactured using low cost techniques which may be integrated with a storage battery without an excessive increase in the cost to produce the battery.

Figure 1 is a side plan view of a storage battery 10 in accordance with the present invention. Storage battery 10 includes a positive terminal 12 and a negative terminal 14. A battery test module 16 is mounted to a housing 18 of the storage battery.

Figures 2A and 2B are top plan views of the storage battery 10 of Figure 1. As illustrated in Figure 2A, battery test module 16 includes an optional input 20 and optional outputs 22 and 24. Input 20 can be, for example, a push button or other input which can be actuated by an operator. Output 22 can be, for example, an LED or other type of visual indicator which provides a pass/fail indication of a battery test. Output 24 can be used to provide a quantitative output of a battery test. In Figure 2B, the output 22 is in the form of a series of outputs 23A, 23B, 23C and 23D which can comprise LEDs.

Figure 3 is a side cross-sectional view of battery 10 taken along the line labeled 3--3 in Figure 2. As illustrated in Figure 3, battery 10 is a storage battery such as a lead-acid battery and includes a number of electrochemical cells which are electrically connected in series by conductors 32. This forms a string of cells 30 having one end electrically coupled to positive terminal 12 through conductor 34 and having the other end electrically coupled to negative terminal 14 through conductor 36. As illustrated in Figure 3, battery test module 16 is coupled to terminals 12 and 14 through two pairs of electrical

-7-

connections which provide Kelvin connections 38 and 40.

In operation, a user can test the condition of battery 10 using battery test module 16. For example, through actuation of button 20 or another input device, a test can be performed on the battery. The results of the battery test are displayed on outputs 22 or 24. In one embodiment, battery test module 16 monitors the battery and waits for a period when the battery is not in use or there is not excessive noise on the electrical system to which the battery is connected and then performs a test on the battery. The results of the battery test can be stored in memory and displayed on output 22 or 24. In such an embodiment, an input such as input 20 is not required to activate the test. However, in such an embodiment, the circuitry within test module 16 could cause the battery to discharge over an extended period.

In the embodiment shown in Figure 2B, battery test module 16 compares the voltage between terminals 12 and 14 to a number of different threshold voltages. Depending upon the voltage of battery 10, an appropriate number of LEDs 23A-D are illuminated on test module 16. For example, each LED can correspond to a different threshold. These thresholds can be spaced as desired. The LEDs 23A-D can also be of different colors. For example, 23A can be a red LED while 23D can be a green LED. In a slightly more complex embodiment, a load, such as a load resistance, in module 16 can be applied to battery 10 during or prior to a voltage measurement. The output of module 16 can be a function of the applied load.

-8-

In one embodiment, test module 16 illuminates outputs 23A-D consecutively until the appropriate threshold is reached. To provide a more desirable user-interface, a small delay can be introduced between the illumination of the each LED. The timing can be as appropriate. The results of the battery test can be maintained on outputs 23A-D for a desired length of time, preferably sufficiently long for a user to observe the test result. In one embodiment, the appropriate number of LEDs remain lit until the test is complete. In another embodiment, only a single LED is lit at a time. Of course, any number of LEDs and thresholds may be used. In other embodiments, additional information can be communicated to an operator by flashing LEDs providing a code or a warning.

The circuitry of the battery tester in the embodiment of Figure 2B can be implemented using simple comparators and timing circuits as will be apparent to those skilled in the art. A more complex embodiment can include a small microprocessor. Typically, the circuitry of battery test module 16 is powered by storage battery 10.

Figure 4 shows a more detailed view of the electrical connections between battery test module 16 and the cells 30 of the battery 10. Cells 30 are illustrated using the electrical symbol for a battery.

Battery test module 16 is coupled to electrochemical cells 30 through Kelvin connections 38 and 40.

A microprocessor in battery test module 16 can store information in memory 44 for later retrieval. For example, information regarding the history of battery usage and battery charging can be

-9-

maintained in memory for later output. A special access code can be entered through user input 20 to cause the data to be output through output 22 or 24 or other output. In one embodiment, the output can be an audio output such as a series of tones or pre-recorded words. The input can comprise a special series of buttons or timing of pressing of buttons. Alternative inputs can also be provided such as an IR sensor, a vibration sensor, a magnetic switch, a proximity receiver which inductively couples to an external device or others. The output can be provided by energizing an LED in accordance with a digital code which could be read by an external device. Other types of outputs can be provided through an IR link, a proximity communication technique such as inductive coupling, etc. Inputs and outputs can also be provided to test module 16 by modulating data onto positive and negative terminals 12 and 14. The data can be received or transmitted using transmit and receive circuitry in battery module 16. Various modulation techniques are known in the art. In one embodiment, the modulation technique is selected such that it does not interfere with external circuitry to which battery 10 may be coupled.

25           The data recording and reporting technique allows a manufacturer to monitor usage of a battery. For example, the manufacturer could determine that the battery was left in an uncharged condition for an extended period, prior to sale, which caused damage to the battery. The data stored in memory can be keyed to date information if such information is maintained by a microprocessor in battery test module 16 such that various events in the life of the battery 18 can be

-10-

linked to specific dates. Examples of other information which can be stored in memory 44 include the date of manufacture, battery ratings, battery serial number of other identification, distribution chain, etc.

Figure 4 also illustrates another aspect of the present invention. In Figure 4, element 10 can also illustrate a standby jumper or auxiliary system 10 which contains an internal battery 30. Jumper cables or other output such as a cigarette lighter adapter, can couple to battery 30 and can be used to provide auxiliary power to an automotive vehicle. For example, such a system can be used to provide a brief charge to a vehicle or to start a vehicle having a dead battery. This can be used to "jump start" the vehicle. Such devices are known in the art and are typically small, portable devices which contain an internal battery. The internal battery can be, for example, a gel cell, a NICAD battery, a nickel metal hydrate battery or other type of battery. One problem with such auxiliary power systems is that the internal battery can fail without the knowledge of the user. When use of the of auxiliary power system is required, the battery may have failed. Further, the type of a failure may be one which is not easily detected in that the battery may provide a normal voltage output but is not capable of supplying a great deal of current for any period of time. With the present invention, system 10 can also include a test module 16 for testing battery 30. In such an embodiment, a user could periodically test battery 30 to ensure it has not failed. Further, test module 16 can periodically test battery 30 and provide a warning indication such

-11-

as a flashing light or a warning sound if battery 30 fails. In one aspect of the invention, any type of battery tester can be used to test such an auxiliary battery system.

5           The present invention can be implemented using any appropriate technique. One example is set forth in U.S. Patent No. 6,172,505, issued January 9, 2001, and entitled ELECTRONIC BATTERY TESTER which is incorporated herein by reference.

10           In one aspect, the battery test module determines battery condition based upon a dynamic parameter of the battery, that is a measurement of the battery which is made using a time varying forcing function F as shown in Figure 4. The resultant signal  
15   S in Figure 4 can be used to determine the dynamic parameter. Example dynamic parameters include dynamic conductance, resistance, impedance and admittance. In another example, single contacts are used to obtain a measurement across the battery.

20           Memory such as memory 44 within test module 16 can be used to store battery specific information such as the rating of battery 10. The information can be loaded into permanent memory during manufacture. Thus, the user is not required to enter any  
25   information regarding the battery. This information can be used in performing the battery test and to provide a qualitative output to a user.

          Output 22 can be any type of output including a visual output. Examples include bi or tri-  
30   color LEDs. The color along with a flashing condition of an LED can indicate test results such as good, bad, low charge, too low to test, or other conditions and determinations. A flashing LED can be used to indicate

-12-

system noise, bad cell, or other conditions and determinations. When the user input 20 is used, the circuitry does not provide any drain on the battery except when activated. However, an input such a switch  
5 can increase cost and could allow a user to attempt a test at an inopportune time, such as during periods of high system noise.

In embodiments without input 20, test module 16 can wait for a quiet time or other appropriate time  
10 to perform a test. The result can be stored in internal memory and periodically displayed on output 22/24 for a brief period. However, extended operation of the test module can drain the battery. In one embodiment, a start-up circuit can be triggered to  
15 'wake up' the test module when the battery experiences a voltage increase such as that due to charging of the battery. The circuitry can then enter a 'sleep' mode based during period of non-charging in order to save power, for example, shortly after charging stops.

20 The battery test module of the present invention is preferable integral with the battery. For example, the module can be mounted to the housing such as to a top cover of the housing. In various embodiments, the module can be carried within the  
25 housing or within an isolated compartment in the housing. The Kelvin connections can couple to the battery terminals either through external or internal  
conductors.

Of course, the test circuitry and test  
30 module can be attached to the battery through any technique including for example, techniques that do not require any modifications to the battery container. For example, it can attach under bolts used

-13-

on the battery post or can use a press fit or "trap" configuration to fit over the battery posts. This allows the circuitry to be optionally added to existing batteries.

5 Further, one aspect of the invention includes any tester that is integral with the battery or substantially permanently attached to the battery that provides an output related to a battery condition such as cold cranking amps (CCA) and/or uses Kelvin  
10 connections to couple to the battery.

Figure 5 is a simplified circuit diagram of test module 16. Module 16 is shown coupled to battery 10. Module 16 operates in accordance with one embodiment of the present invention and determines the  
15 conductance ( $G_{BAT}$ ) of battery 10 and the voltage potential ( $V_{BAT}$ ) between terminals 12 and 14. Module 16 includes current source 50, differential amplifier 52, analog-to-digital converter 54 and microprocessor 56. Amplifier 52 is capacitively coupled to battery  
20 10 through capacitors  $C_1$  and  $C_2$ . Amplifier 52 has an output connected to an input of analog-to-digital converter 54. Microprocessor 56 is connected to system clock 58, memory 60, visual output 62 and analog-to-digital converter 54. Microprocessor 56 is  
25 also capable of receiving an input from input device 66. Further, an input/output (I/O) port 67 is provided.

---

In operation, current source 50 is controlled by microprocessor 56 and provides a current  
30 in the direction shown by the arrow in Figure 5. In one embodiment, this is a square wave or a pulse. Differential amplifier 52 is connected to terminals 22 and 24 of battery 10 through capacitors  $C_1$  and  $C_2$ ,

-14-

respectively, and provides an output related to the voltage potential difference between terminals 12 and 14. In a preferred embodiment, amplifier 52 has a high input impedance. Circuitry 16 includes  
5 differential amplifier 70 having inverting and noninverting inputs connected to terminals 24 and 22, respectively. Amplifier 70 is connected to measure the open circuit potential voltage ( $V_{BAT}$ ) of battery 10  
10 70 is provided to analog-to-digital converter 54 such that the voltage across terminals 12 and 14 can be measured by microprocessor 56.

Module 16 is connected to battery 10 through a four-point connection technique known as a Kelvin  
15 connection. This Kelvin connection allows current  $I$  to be injected into battery 10 through a first pair of terminals while the voltage  $V$  across the terminals 12 and 14 is measured by a second pair of connections. Because very little current flows through amplifier  
20 52, the voltage drop across the inputs to amplifier 52 is substantially identical to the voltage drop across terminals 12 and 14 of battery 12. The output of differential amplifier 52 is converted to a digital format and is provided to microprocessor 56.  
25 Microprocessor 56 operates at a frequency determined by system clock 58 and in accordance with programming instructions stored in memory 60.

Microprocessor 56 determines the conductance of battery 10 by applying a current pulse  $I$  using  
30 current source 50. The microprocessor determines the change in battery voltage due to the current pulse  $I$  using amplifier 52 and analog-to-digital converter 54. The value of current  $I$  generated by current source 50

-15-

is known and is stored in memory 60. In one embodiment, current I is obtained by applying a load to battery 10. Microprocessor 56 calculates the conductance of battery 10 using the following equation:

$$\text{Conductance} = G_{\text{BAT}} = \frac{\Delta I}{\Delta V} \quad \text{Equation 1}$$

where  $\Delta I$  is the change in current flowing through battery 10 due to current source 50 and  $\Delta V$  is the change in battery voltage due to applied current  $\Delta I$ . A temperature sensor 62 can be thermally coupled to battery 10 and used to compensate battery measurements. Temperature readings can be stored in memory 60 for later retrieval.

Figure 6 is a simple diagram for the embodiment of module 16 shown in Figure 2B. A comparator 90 can periodically compare a voltage measurement to a plurality of reference levels and responsively energize LEDs 23A-D to provide an indication of the condition of battery 10. This display can be provided or be activated by a switch or other condition. Any of the various features set forth in the Figures and discussion can be used in any appropriate combination and should not be limited to the specific examples shown.

Although the present invention has been described with reference to preferred embodiments, workers skilled in the art will recognize that changes may be made in form and detail without departing from the spirit and scope of the invention.

-16-

WHAT IS CLAIMED IS:

1. A storage battery, comprising:  
a battery housing;  
a plurality of electrochemical cells in the battery housing electrically connected in series to a positive terminal of the battery and a negative terminal of the battery;  
a first Kelvin connection coupled to the positive terminal of the battery;  
a second Kelvin connection coupled to the negative terminal of the battery;  
a battery test module mounted to the battery housing and electrically coupled to the positive and negative terminals through respective first and second Kelvin connection; and  
an output from the test module configured to output a battery test result from the battery test module.
2. The storage battery of claim 1 wherein the battery test module is configured to output information stored in a memory.
3. The storage battery of claim 1 wherein the test module is attached to the battery after manufacture of the battery.
4. The storage battery of claim 2 wherein the  
information stored in a memory is output through an electrical connection.
5. The storage battery of claim 1 wherein the Kelvin connections are external to the battery housing.

-17-

6. The storage battery of claim 1 wherein the Kelvin connections are internal to the battery housing.

7. The storage battery of claim 3 wherein the test module is attached to posts of the battery.

8. The storage battery of claim 1 wherein the test result is related to battery cold cranking amps (CCA).

9. The storage battery of claim 1 wherein the test result is related to a dynamic parameter of the battery.

10. The storage battery of claim 1 wherein the dynamic parameter is determined as a function of an electrical load to the battery.

11. The storage battery of claim 1 including a temperature sensor thermally coupled to the battery.

12. The storage battery of claim 11 wherein temperature measurements from the temperature sensor are stored in a memory.

13. The storage battery of claim 11 wherein the battery test result is a function of the measured temperature.

14. The storage battery of claim 1 wherein the output comprises at least one LED.

15. The storage battery of claim 1 including a user operable switch configured to initiate output of a battery test result.

16. The storage battery of claim 15 wherein the output battery test result is retrieved from a memory.

17. The storage battery of claim 1 wherein a battery test is performed during periods of non-use of the battery.

-18-

18. The storage battery of claim 1 wherein the battery test is performed during periods of reduced noise in the battery.

19. The storage battery of claim 14 wherein the at least one LED is turned on and off in a sequence to convey information.

20. The storage battery of claim 1 wherein the output comprises an audio output.

21. The storage battery of claim 2 including a switch and wherein information is retrieved from the memory only after receipt of a coded input through the switch.

22. The storage battery of claim 1 wherein the output comprises an IR link.

23. The storage battery of claim 1 wherein the output was a proximity communication technique.

24. The storage battery of claim 1 wherein the output is modulated onto a terminal of the battery.

25. The storage battery of claim 2 wherein information stored in the memory includes information related to a date.

26. The storage battery of claim 1 including a memory which stores information related to a rating of the battery and the battery test result is a function of the stored information.

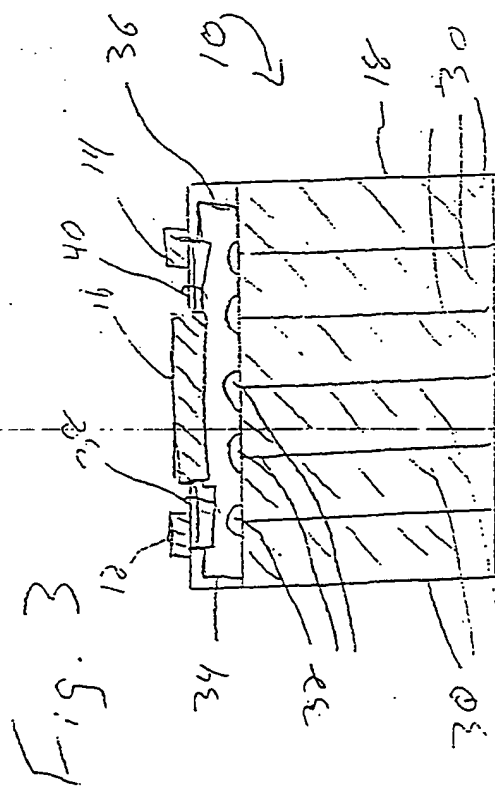
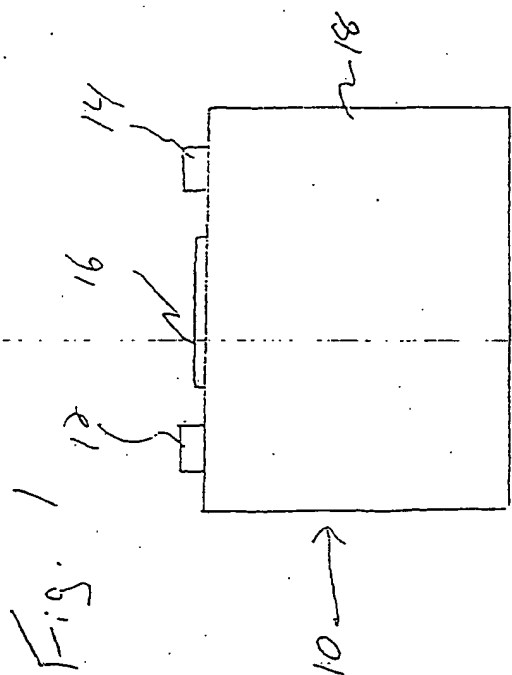
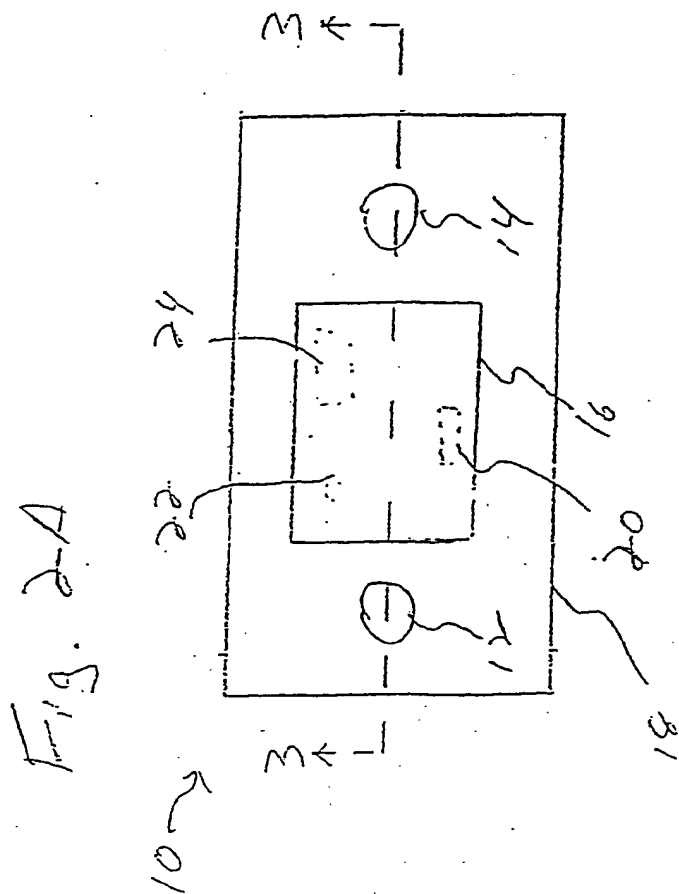
27. An auxiliary power system, comprising:

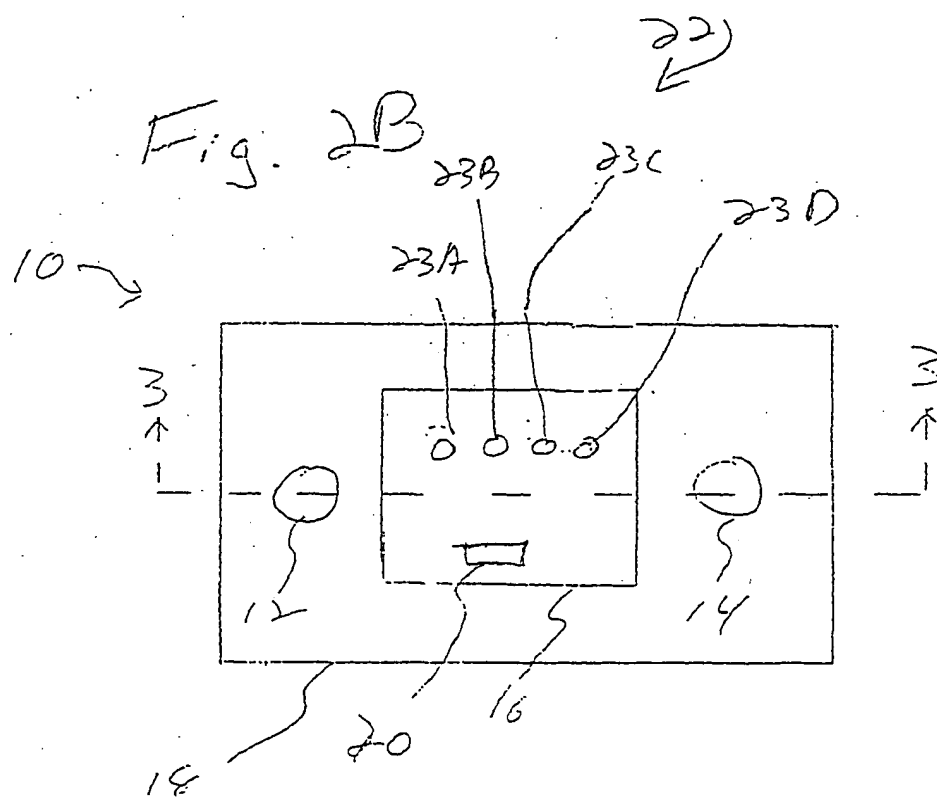
an auxiliary battery;  
a battery test module electrically coupled to the auxiliary battery and configured to perform a battery test on the auxiliary battery and responsively provide a battery test output; and

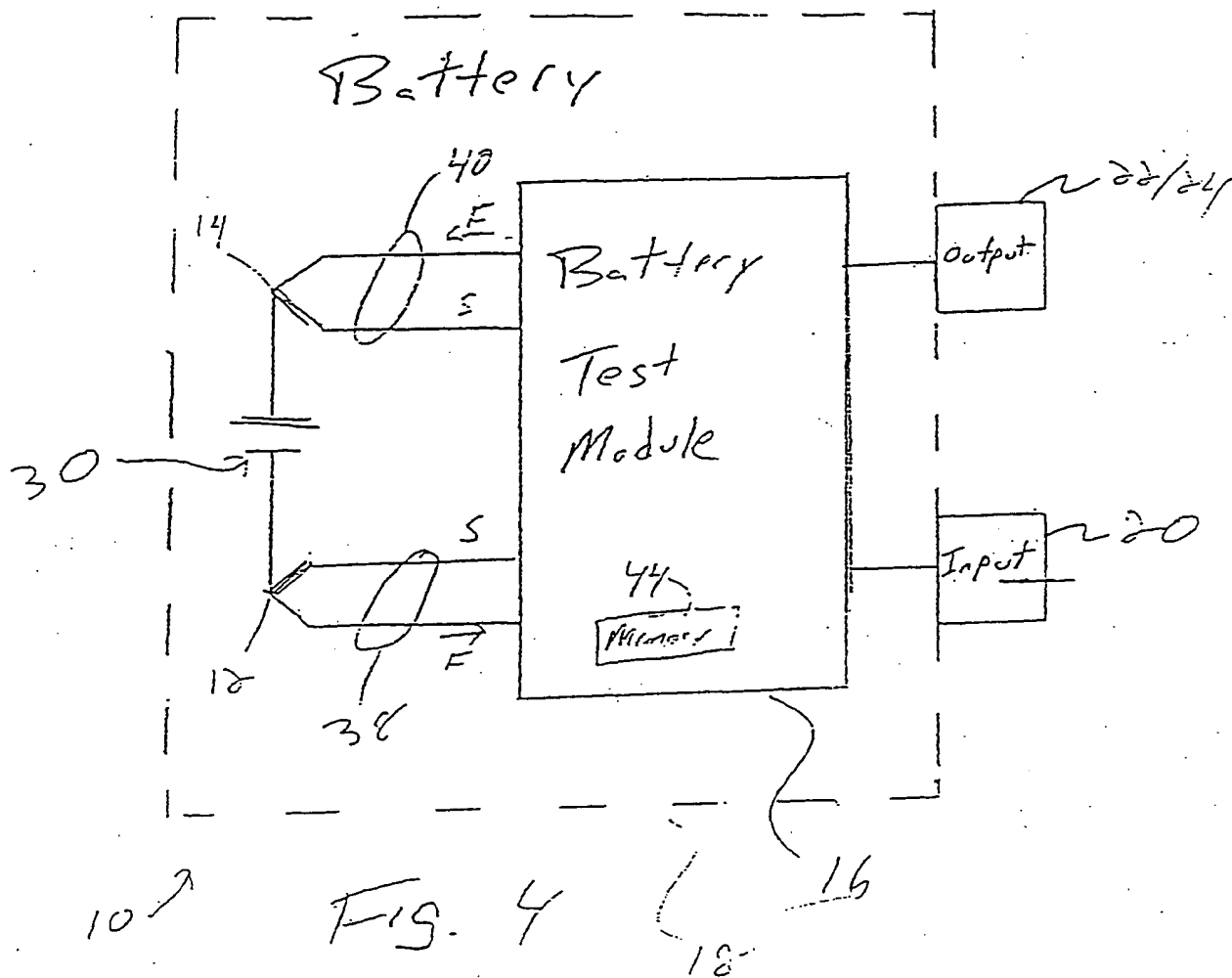
-19-

an output configured to output results of  
the battery test output.

---







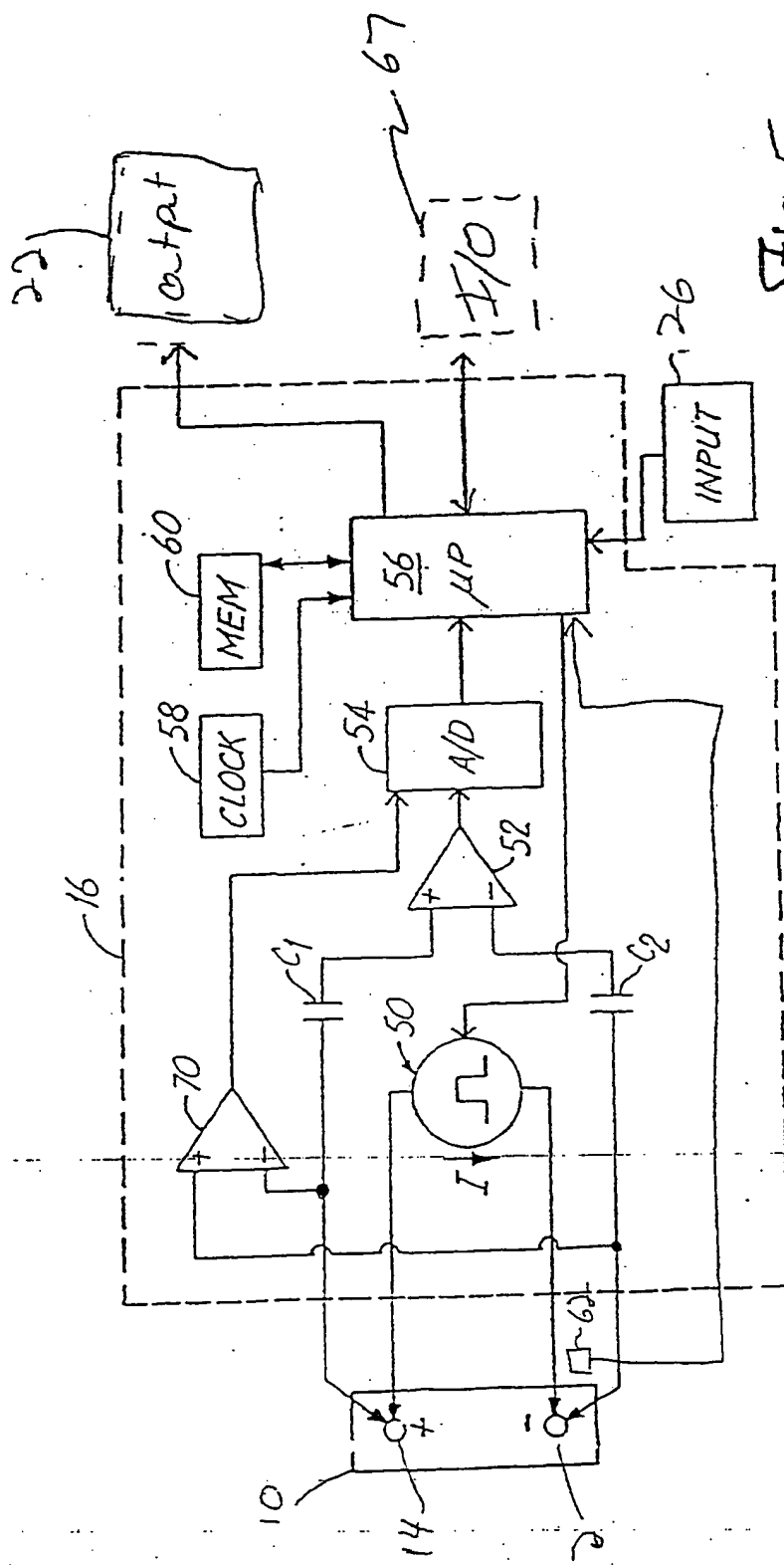


Fig. 5

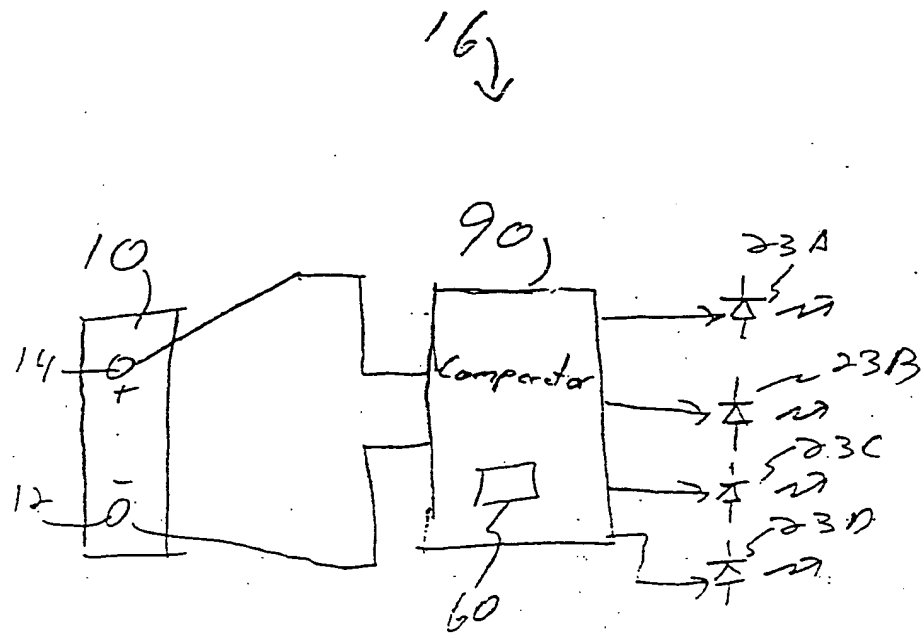


Fig. 6

## INTERNATIONAL SEARCH REPORT

International application No.  
PCT/US01/04193

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G01N 27/416

US CL : 324/429

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 324/429, 430, 434; 320/120, 132, 134

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
NONEElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
APS, EPOABS

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6,127,505 B1 (BERTNESS) 09 January 2001 (09.01.2001), see entire document.	1-27
X	US 6,104,167 A (BERTNESS et al.) 15 August 2000 (15.08.2000), see entire document.	1-27
X	US 6,091,245 A (BERTNESS) 18 July 2000 (18.07.2000), see entire document.	1-27
X	US 6,051,976 A (BERTNESS) 18 April 2000 (18.04.2000), see entire document.	1-27

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
*A* document defining the general state of the art which is not considered to be of particular relevance	*X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
*E* earlier document published on or after the international filing date	*Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
*L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	*Z* document member of the same patent family
*O* document referring to an oral disclosure, use, exhibition or other means	
*P* document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

03 APRIL 2001

Date of mailing of the international search report

23 APR 2001

Name and mailing address of the ISA/US  
Commissioner of Patents and Trademarks  
Box PCT  
Washington, D.C. 20231

Facsimile No. (703) 305-3230

Authorized officer

GREGORY J. TOATLEY, JR.

Telephone No. (703) 308-1782

1. The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that proper record-keeping is essential for the transparency and accountability of the organization. This section also outlines the various methods used to collect and analyze data, ensuring that the information is reliable and up-to-date.

2. The second part of the document focuses on the implementation of the proposed changes. It details the steps involved in the rollout process, from initial planning to final execution. This section also addresses potential challenges and provides strategies to overcome them, ensuring a smooth transition to the new system.

3. The third part of the document discusses the ongoing monitoring and evaluation of the project. It highlights the need for continuous communication and collaboration between all stakeholders involved. This section also provides a framework for assessing the progress and impact of the project, allowing for timely adjustments and improvements.

4. The final part of the document concludes with a summary of the key findings and recommendations. It reiterates the importance of maintaining accurate records and the need for ongoing communication and collaboration. The document also provides a list of resources and contacts for further information and support.

---

5. The first part of the document discusses the importance of maintaining accurate records of all transactions. It emphasizes that proper record-keeping is essential for the transparency and accountability of the organization. This section also outlines the various methods used to collect and analyze data, ensuring that the information is reliable and up-to-date.

6. The second part of the document focuses on the implementation of the proposed changes. It details the steps involved in the rollout process, from initial planning to final execution. This section also addresses potential challenges and provides strategies to overcome them, ensuring a smooth transition to the new system.

7. The third part of the document discusses the ongoing monitoring and evaluation of the project. It highlights the need for continuous communication and collaboration between all stakeholders involved. This section also provides a framework for assessing the progress and impact of the project, allowing for timely adjustments and improvements.

8. The final part of the document concludes with a summary of the key findings and recommendations. It reiterates the importance of maintaining accurate records and the need for ongoing communication and collaboration. The document also provides a list of resources and contacts for further information and support.

CORRECTED VERSION

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
16 August 2001 (16.08.2001)

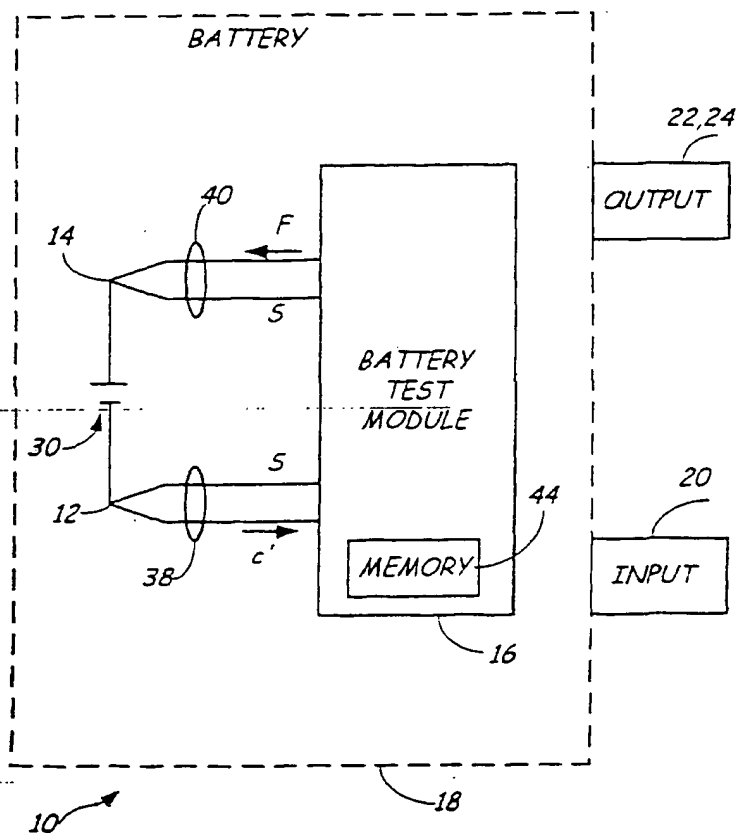
PCT

(10) International Publication Number  
WO 01/059443 A1

- (51) International Patent Classification<sup>7</sup>: G01N 27/416 (71) Applicant: MIDTRONICS, INC. [US/US]; 7000 Monroe Street, Willowbrook, IL 60521 (US).
- (21) International Application Number: PCT/US01/04193 (72) Inventors: BERTNESS, Kevin, I.; 1317 McClurg Drive, Batavia, IL 60510 (US). BUTTERIS, Jamey; 7912 Woodlyn Drive, Apt. 103, Woodridge, IL 60517 (US).
- (22) International Filing Date: 9 February 2001 (09.02.2001)
- (25) Filing Language: English (74) Agents: CHAMPLIN, Judson, K. et al.; Westman, Champlin & Kelly, P.A., Suite 1600 - International Centre, 900 Second Avenue South, Minneapolis, MN 55402-3319 (US).
- (26) Publication Language: English
- (30) Priority Data:
- |            |                               |    |
|------------|-------------------------------|----|
| 60/181,854 | 11 February 2000 (11.02.2000) | US |
| 09/544,696 | 7 April 2000 (07.04.2000)     | US |
| 60/204,345 | 15 May 2000 (15.05.2000)      | US |
| 60/218,878 | 18 July 2000 (18.07.2000)     | US |
| 60/224,092 | 9 August 2000 (09.08.2000)    | US |
- (81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ,

[Continued on next page]

(54) Title: STORAGE BATTERY WITH INTEGRAL BATTERY TESTER



(57) Abstract: A storage battery (10) includes a battery housing (18) and a plurality of electrochemical cells (30) in the battery housing electrically connected in series to a positive terminal (12) of the battery and a negative terminal (14) of the battery. A first connection (38) is coupled to the positive terminal of the battery and a second connection (40) is coupled to the negative terminal of the battery. A battery test module is mounted to the battery housing and electrically coupled to the positive and negative terminals through the respective first and second Kelvin connections. A display or other output is configured to output a battery test result from the battery test module.

WO 01/059443 A1



NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM,  
TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.

(48) Date of publication of this corrected version:

24 October 2002

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MP, NE, SN, TD, TG).

(15) Information about Correction:

see PCT Gazette No. 43/2002 of 24 October 2002, Section II

Published:

— with international search report

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

-1-

## STORAGE BATTERY WITH INTEGRAL BATTERY TESTER

### BACKGROUND OF THE INVENTION

The present invention relates to storage  
5 batteries. More specifically, the present invention  
relates to storage batteries with integral battery  
testers.

Storage batteries, such as lead acid storage  
batteries, are used in a variety of applications such  
10 as automotive vehicles and standby power sources.  
Typical storage batteries consist of a plurality of  
individual storage cells which are electrically  
connected in series. Each cell can have a voltage  
potential of about 2.1 volts, for example. By  
15 connecting the cells in the series, the voltages of  
the individual cells are added in a cumulative manner.  
For example, in a typical automotive storage battery,  
six storage cells are used to provide a total voltage  
of about 12.6 volts. The individual cells are held in  
20 a housing and the entire assembly is commonly referred  
to as the "battery."

It is frequently desirable to ascertain the  
condition of a storage battery. Various testing  
techniques have been developed over the long history  
25 of storage batteries. For example, one technique  
involves the use of a hygrometer in which the specific  
gravity of the acid mixture in the battery is  
measured. Electrical testing has also been used to  
provide less invasive battery testing techniques. A  
30 very simple electrical test is to simply measure the  
voltage across the battery. If the voltage is below a  
certain threshold, the battery is determined to be  
bad. Another technique for testing a battery is

-2-

referred to as a load test. In a load test, the battery is discharged using a known load. As the battery is discharged, the voltage across the battery is monitored and used to determine the condition of the battery. More recently, a technique has been pioneered by Dr. Keith S. Champlin and Midtronics, Inc. of Willowbrook, Illinois for testing storage battery by measuring a dynamic parameter of the battery such as the dynamic conductance of the battery. This technique is described in a number of United States patents, for example, U.S. Patent No. 3,873,911, issued March 25, 1975, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 3,909,708, issued September 30, 1975, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,816,768, issued March 28, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE; U.S. Patent No. 4,825,170, issued April 25, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH AUTOMATIC VOLTAGE SCALING; U.S. Patent No. 4,881,038, issued November 14, 1989, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH AUTOMATIC VOLTAGE SCALING TO DETERMINE DYNAMIC CONDUCTANCE; U.S. Patent No. 4,912,416, issued March 27, 1990, to Champlin, entitled ELECTRONIC BATTERY TESTING DEVICE WITH STATE-OF-CHARGE COMPENSATION; U.S. Patent No. 5,140,269, issued August 18, 1992, to Champlin, entitled ELECTRONIC TESTER FOR ASSESSING BATTERY/CELL CAPACITY; U.S. Patent No. 5,343,380, issued August 30, 1994, entitled METHOD AND APPARATUS FOR SUPPRESSING TIME VARYING SIGNALS IN BATTERIES UNDERGOING CHARGING OR DISCHARGING; U.S. Patent No. 5,572,136, issued November 5, 1996, entitled

-3-

ELECTRONIC BATTERY TESTER WITH AUTOMATIC COMPENSATION  
FOR LOW STATE-OF-CHARGE; U.S. Patent No. 5,574,355,  
issued November 12, 1996, entitled METHOD AND  
APPARATUS FOR DETECTION AND CONTROL OF THERMAL RUNAWAY  
5 IN A BATTERY UNDER CHARGE; U.S. Patent No. 5,585,728,  
issued December 17, 1996, entitled ELECTRONIC BATTERY  
TESTER WITH AUTOMATIC COMPENSATION FOR LOW STATE-OF-  
CHARGE; U.S. Patent No. 5,592,093, issued January 7,  
1997, entitled ELECTRONIC BATTERY TESTING DEVICE LOOSE  
10 TERMINAL CONNECTION DETECTION VIA A COMPARISON  
CIRCUIT; U.S. Patent No. 5,598,098, issued January 28,  
1997, entitled ELECTRONIC BATTERY TESTER WITH VERY  
HIGH NOISE IMMUNITY; U.S. Patent No. 5,757,192, issued  
May 26, 1998, entitled METHOD AND APPARATUS FOR  
15 DETECTING A BAD CELL IN A STORAGE BATTERY; U.S. Patent  
No. 5,821,756, issued October 13, 1998, entitled  
ELECTRONIC BATTERY TESTER WITH TAILORED COMPENSATION  
FOR LOW STATE-OF-CHARGE; U.S. Patent No. 5,831,435,  
issued November 3, 1998, entitled BATTERY TESTER FOR  
20 JIS STANDARD; U.S. Patent No. 5,914,605, issued June  
22, 1999, entitled ELECTRONIC BATTERY TESTER; U.S.  
Patent No. 5,945,829, issued August 31, 1999, entitled  
MIDPOINT BATTERY MONITORING; U.S. Patent No.  
6,002,238, issued December 14, 1999, entitled METHOD  
25 AND APPARATUS FOR MEASURING COMPLEX IMPEDANCE OF CELLS  
AND BATTERIES; U.S. Patent No. 6,037,777, issued March  
14, 2000, entitled METHOD AND APPARATUS FOR  
DETERMINING BATTERY PROPERTIES FROM COMPLEX  
IMPEDANCE/ADMITTANCE; U.S. Patent No. 6,051,976,  
30 issued April 18, 2000, entitled METHOD AND APPARATUS  
FOR AUDITING A BATTERY TEST; U.S. Patent No.  
6,081,098, issued June 27, 2000, entitled METHOD AND  
APPARATUS FOR CHARGING A BATTERY; U.S. Patent No.

-4-

6,091,245, issued July 18, 2000, entitled METHOD AND APPARATUS FOR AUDITING A BATTERY TEST; U.S. Patent No. 6,104,167, issued August 15, 2000, entitled METHOD AND APPARATUS FOR CHARGING A BATTERY; U.S. Patent No. 5 6,137,269, issued October 24, 2000, entitled METHOD AND APPARATUS FOR ELECTRONICALLY EVALUATING THE INTERNAL TEMPERATURE OF AN ELECTROCHEMICAL CELL OR BATTERY; U.S. Patent No. 6,163,156, issued December 19, 2000, entitled ELECTRICAL CONNECTION FOR 10 ELECTRONIC BATTERY TESTER; U.S. Patent No. 6,172,483, issued January 9, 2001, entitled METHOD AND APPARATUS FOR MEASURING COMPLEX IMPEDANCE OF CELL AND BATTERIES; and U.S. Patent No. 6,172,505, issued January 9, 2001, entitled ELECTRONIC BATTERY TESTER.

15 In general, battery testers have been separate pieces of equipment which can be moved between storage batteries and electrically coupled to a storage battery. The prior art has lacked a simple technique for the testing of a storage battery without 20 relying on separate testing equipment.

#### SUMMARY OF THE INVENTION

A storage battery includes a battery housing and a plurality of electrochemical cells in the battery housing electrically connected in series to a 25 positive terminal of the battery and a negative terminal of the battery. A first connection is coupled to the positive terminal of the battery and a second connection is coupled to the negative terminal of the battery. A battery test module is mounted to the 30 battery housing and electrically coupled to the positive and negative terminals through the respective first and second Kelvin connection. A display or other

-5-

output is configured to output a battery test result from the battery test module.

#### BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a side plan view of a storage battery including a battery test module in accordance with the present invention.

Figures 2A and 2B are top plan views of the storage battery of Figure 1.

Figure 3 is a side cross-sectional view of the storage battery of Figures 1 and 2 taken along the line labeled 3--3 in Figure 2.

Figure 4 is a block diagram of a storage battery in accordance with the present invention.

Figure 5 is an electrical diagram of one example embodiment.

Figure 6 is an electrical diagram of another example embodiment.

#### DETAILED DESCRIPTION OF THE ILLUSTRATIVE EMBODIMENTS

In one aspect of the present invention a storage battery is provided having an integrated battery test module for performing a battery test on electrical cells of the storage battery. As used herein "integrated" can include a separate module which is attached to the battery housing. In one embodiment, the battery test module is electrically coupled to the electrical cells of the storage battery through Kelvin connections. In certain aspects, Kelvin connections are not used. As the battery test module is integral with the battery, an operator can test the battery without relying on external battery test equipment. In one embodiment, the battery test is one that can be easily performed by an unskilled operator. Further, the battery test module is preferably

-6-

manufactured using low cost techniques which may be integrated with a storage battery without an excessive increase in the cost to produce the battery.

Figure 1 is a side plan view of a storage battery 10 in accordance with the present invention. Storage battery 10 includes a positive terminal 12 and a negative terminal 14. A battery test module 16 is mounted to a housing 18 of the storage battery.

Figures 2A and 2B are top plan views of the storage battery 10 of Figure 1. As illustrated in Figure 2A, battery test module 16 includes an optional input 20 and optional outputs 22 and 24. Input 20 can be, for example, a push button or other input which can be actuated by an operator. Output 22 can be, for example, an LED or other type of visual indicator which provides a pass/fail indication of a battery test. Output 24 can be used to provide a quantitative output of a battery test. In Figure 2B, the output 22 is in the form of a series of outputs 23A, 23B, 23C and 23D which can comprise LEDs.

Figure 3 is a side cross-sectional view of battery 10 taken along the line labeled 3--3 in Figure 2. As illustrated in Figure 3, battery 10 is a storage battery such as a lead-acid battery and includes a number of electrochemical cells which are electrically connected in series by conductors 32. This forms a string of cells 30 having one end electrically coupled to positive terminal 12 through conductor 34 and having the other end electrically coupled to negative terminal 14 through conductor 36. As illustrated in Figure 3, battery test module 16 is coupled to terminals 12 and 14 through two pairs of electrical

-7-

connections which provide Kelvin connections 38 and 40.

In operation, a user can test the condition of battery 10 using battery test module 16. For example, through actuation of button 20 or another input device, a test can be performed on the battery. The results of the battery test are displayed on outputs 22 or 24. In one embodiment, battery test module 16 monitors the battery and waits for a period when the battery is not in use or there is not excessive noise on the electrical system to which the battery is connected and then performs a test on the battery. The results of the battery test can be stored in memory and displayed on output 22 or 24. In such an embodiment, an input such as input 20 is not required to activate the test. However, in such an embodiment, the circuitry within test module 16 could cause the battery to discharge over an extended period.

In the embodiment shown in Figure 2B, battery test module 16 compares the voltage between terminals 12 and 14 to a number of different threshold voltages. Depending upon the voltage of battery 10, an appropriate number of LEDs 23A-D are illuminated on test module 16. For example, each LED can correspond to a different threshold. These thresholds can be spaced as desired. The LEDs 23A-D can also be of different colors. For example, 23A can be a red LED while 23D can be a green LED. In a slightly more complex embodiment, a load, such as a load resistance, in module 16 can be applied to battery 10 during or prior to a voltage measurement. The output of module 16 can be a function of the applied load.

-8-

In one embodiment, test module 16 illuminates outputs 23A-D consecutively until the appropriate threshold is reached. To provide a more desirable user-interface, a small delay can be introduced between the illumination of the each LED. The timing can be as appropriate. The results of the battery test can be maintained on outputs 23A-D for a desired length of time, preferably sufficiently long for a user to observe the test result. In one embodiment, the appropriate number of LEDs remain lit until the test is complete. In another embodiment, only a single LED is lit at a time. Of course, any number of LEDs and thresholds may be used. In other embodiments, additional information can be communicated to an operator by flashing LEDs providing a code or a warning.

The circuitry of the battery tester in the embodiment of Figure 2B can be implemented using simple comparators and timing circuits as will be apparent to those skilled in the art. A more complex embodiment can include a small microprocessor. Typically, the circuitry of battery test module 16 is powered by storage battery 10.

Figure 4 shows a more detailed view of the electrical connections between battery test module 16 and the cells 30 of the battery 10. Cells 30 are illustrated using the electrical symbol for a battery. Battery test module 16 is coupled to electrochemical cells 30 through Kelvin connections 38 and 40.

A microprocessor in battery test module 16 can store information in memory 44 for later retrieval. For example, information regarding the history of battery usage and battery charging can be

-9-

maintained in memory for later output. A special access code can be entered through user input 20 to cause the data to be output through output 22 or 24 or other output. In one embodiment, the output can be an audio output such as a series of tones or pre-recorded words. The input can comprise a special series of buttons or timing of pressing of buttons. Alternative inputs can also be provided such as an IR sensor, a vibration sensor, a magnetic switch, a proximity receiver which inductively couples to an external device or others. The output can be provided by energizing an LED in accordance with a digital code which could be read by an external device. Other types of outputs can be provided through an IR link, a proximity communication technique such as inductive coupling, etc. Inputs and outputs can also be provided to test module 16 by modulating data onto positive and negative terminals 12 and 14. The data can be received or transmitted using transmit and receive circuitry in battery module 16. Various modulation techniques are known in the art. In one embodiment, the modulation technique is selected such that it does not interfere with external circuitry to which battery 10 may be coupled.

25           The data recording and reporting technique allows a manufacturer to monitor usage of a battery. For example, the manufacturer could determine that the battery was left in an uncharged condition for an extended period, prior to sale, which caused damage to the battery. The data stored in memory can be keyed to date information if such information is maintained by a microprocessor in battery test module 16 such that various events in the life of the battery 18 can be

-10-

linked to specific dates. Examples of other information which can be stored in memory 44 include the date of manufacture, battery ratings, battery serial number of other identification, distribution chain, etc.

Figure 4 also illustrates another aspect of the present invention. In Figure 4, element 10 can also illustrate a standby jumper or auxiliary system 10 which contains an internal battery 30. Jumper cables or other output such as a cigarette lighter adapter, can couple to battery 30 and can be used to provide auxiliary power to an automotive vehicle. For example, such a system can be used to provide a brief charge to a vehicle or to start a vehicle having a dead battery. This can be used to "jump start" the vehicle. Such devices are known in the art and are typically small, portable devices which contain an internal battery. The internal battery can be, for example, a gel cell, a NICAD battery, a nickel metal hydrate battery or other type of battery. One problem with such auxiliary power systems is that the internal battery can fail without the knowledge of the user. When use of the of auxiliary power system is required, the battery may have failed. Further, the type of a failure may be one which is not easily detected in that the battery may provide a normal voltage output but is not capable of supplying a great deal of current for any period of time. With the present invention, system 10 can also include a test module 16 for testing battery 30. In such an embodiment, a user could periodically test battery 30 to ensure it has not failed. Further, test module 16 can periodically test battery 30 and provide a warning indication such

-11-

as a flashing light or a warning sound if battery 30 fails. In one aspect of the invention, any type of battery tester can be used to test such an auxiliary battery system.

5           The present invention can be implemented using any appropriate technique. One example is set forth in U.S. Patent No. 6,172,505, issued January 9, 2001, and entitled ELECTRONIC BATTERY TESTER which is incorporated herein by reference.

10           In one aspect, the battery test module determines battery condition based upon a dynamic parameter of the battery, that is a measurement of the battery which is made using a time varying forcing function F as shown in Figure 4. The resultant signal  
15   S in Figure 4 can be used to determine the dynamic parameter. Example dynamic parameters include dynamic conductance, resistance, impedance and admittance. In another example, single contacts are used to obtain a measurement across the battery.

20           Memory such as memory 44 within test module 16 can be used to store battery specific information such as the rating of battery 10. The information can be loaded into permanent memory during manufacture. Thus, the user is not required to enter any  
25   information regarding the battery. This information can be used in performing the battery test and to provide a qualitative output to a user.

          Output 22 can be any type of output including a visual output. Examples include bi or tri-  
30   color LEDs. The color along with a flashing condition of an LED can indicate test results such as good, bad, low charge, too low to test, or other conditions and determinations. A flashing LED can be used to indicate

-12-

system noise, bad cell, or other conditions and determinations. When the user input 20 is used, the circuitry does not provide any drain on the battery except when activated. However, an input such a switch  
5 can increase cost and could allow a user to attempt a test at an inopportune time, such as during periods of high system noise.

In embodiments without input 20, test module 16 can wait for a quiet time or other appropriate time  
10 to perform a test. The result can be stored in internal memory and periodically displayed on output 22/24 for a brief period. However, extended operation of the test module can drain the battery. In one embodiment, a start-up circuit can be triggered to  
15 'wake up' the test module when the battery experiences a voltage increase such as that due to charging of the battery. The circuitry can then enter a 'sleep' mode based during period of non-charging in order to save power, for example, shortly after charging stops.

20 The battery test module of the present invention is preferable integral with the battery. For example, the module can be mounted to the housing such as to a top cover of the housing. In various embodiments, the module can be carried within the  
25 housing or within an isolated compartment in the housing. The Kelvin connections can couple to the battery terminals either through external or internal conductors.

Of course, the test circuitry and test  
30 module can be attached to the battery through any technique including for example, techniques that do not require any modifications to the battery container. For example, it can attach under bolts used

-13-

on the battery post or can use a press fit or "trap" configuration to fit over the battery posts. This allows the circuitry to be optionally added to existing batteries.

5 Further, one aspect of the invention includes any tester that is integral with the battery or substantially permanently attached to the battery that provides an output related to a battery condition such as cold cranking amps (CCA) and/or uses Kelvin  
10 connections to couple to the battery.

Figure 5 is a simplified circuit diagram of test module 16. Module 16 is shown coupled to battery 10. Module 16 operates in accordance with one embodiment of the present invention and determines the  
15 conductance ( $G_{BAT}$ ) of battery 10 and the voltage potential ( $V_{BAT}$ ) between terminals 12 and 14. Module 16 includes current source 50, differential amplifier 52, analog-to-digital converter 54 and microprocessor 56. Amplifier 52 is capacitively coupled to battery  
20 10 through capacitors  $C_1$  and  $C_2$ . Amplifier 52 has an output connected to an input of analog-to-digital converter 54. Microprocessor 56 is connected to system clock 58, memory 60, visual output 62 and analog-to-digital converter 54. Microprocessor 56 is  
25 also capable of receiving an input from input device 66. Further, an input/output (I/O) port 67 is provided.

In operation, current source 50 is controlled by microprocessor 56 and provides a current  
30 in the direction shown by the arrow in Figure 5. In one embodiment, this is a square wave or a pulse. Differential amplifier 52 is connected to terminals 22 and 24 of battery 10 through capacitors  $C_1$  and  $C_2$ ,

-14-

respectively, and provides an output related to the voltage potential difference between terminals 12 and 14. In a preferred embodiment, amplifier 52 has a high input impedance. Circuitry 16 includes  
5 differential amplifier 70 having inverting and noninverting inputs connected to terminals 24 and 22, respectively. Amplifier 70 is connected to measure the open circuit potential voltage ( $V_{BAT}$ ) of battery 10  
10 70 is provided to analog-to-digital converter 54 such that the voltage across terminals 12 and 14 can be measured by microprocessor 56.

Module 16 is connected to battery 10 through a four-point connection technique known as a Kelvin  
15 connection. This Kelvin connection allows current  $I$  to be injected into battery 10 through a first pair of terminals while the voltage  $V$  across the terminals 12 and 14 is measured by a second pair of connections. Because very little current flows through amplifier  
20 52, the voltage drop across the inputs to amplifier 52 is substantially identical to the voltage drop across terminals 12 and 14 of battery 12. The output of differential amplifier 52 is converted to a digital format and is provided to microprocessor 56.  
25 Microprocessor 56 operates at a frequency determined by system clock 58 and in accordance with programming instructions stored in memory 60.

---

Microprocessor 56 determines the conductance of battery 10 by applying a current pulse  $I$  using  
30 current source 50. The microprocessor determines the change in battery voltage due to the current pulse  $I$  using amplifier 52 and analog-to-digital converter 54. The value of current  $I$  generated by current source 50

-15-

is known and is stored in memory 60. In one embodiment, current I is obtained by applying a load to battery 10. Microprocessor 56 calculates the conductance of battery 10 using the following equation:

$$\text{Conductance} = G_{\text{BAT}} = \frac{\Delta I}{\Delta V} \quad \text{Equation 1}$$

where  $\Delta I$  is the change in current flowing through battery 10 due to current source 50 and  $\Delta V$  is the change in battery voltage due to applied current  $\Delta I$ . A temperature sensor 62 can be thermally coupled to battery 10 and used to compensate battery measurements. Temperature readings can be stored in memory 60 for later retrieval.

Figure 6 is a simple diagram for the embodiment of module 16 shown in Figure 2B. A comparator 90 can periodically compare a voltage measurement to a plurality of reference levels and responsively energize LEDs 23A-D to provide an indication of the condition of battery 10. This display can be provided or be activated by a switch or other condition. Any of the various features set forth in the Figures and discussion can be used in any appropriate combination and should not be limited to the specific examples shown.

Although the present invention has been described with reference to preferred embodiments, workers skilled in the art will recognize that changes may be made in form and detail without departing from the spirit and scope of the invention.

-16-

WHAT IS CLAIMED IS:

1. A storage battery, comprising:
  - a battery housing;
  - a plurality of electrochemical cells in the battery housing electrically connected in series to a positive terminal of the battery and a negative terminal of the battery;
  - a first Kelvin connection coupled to the positive terminal of the battery;
  - a second Kelvin connection coupled to the negative terminal of the battery;
  - a battery test module mounted to the battery housing and electrically coupled to the positive and negative terminals through respective first and second Kelvin connection; and
  - an output from the test module configured to output a battery test result from the battery test module.
2. The storage battery of claim 1 wherein the battery test module is configured to output information stored in a memory.
3. The storage battery of claim 1 wherein the test module is attached to the battery after manufacture of the battery.
4. The storage battery of claim 2 wherein the information stored in a memory is output through an electrical connection.
5. The storage battery of claim 1 wherein the Kelvin connections are external to the battery housing.

-17-

6. The storage battery of claim 1 wherein the Kelvin connections are internal to the battery housing.

7. The storage battery of claim 3 wherein the test module is attached to posts of the battery.

8. The storage battery of claim 1 wherein the test result is related to battery cold cranking amps (CCA).

9. The storage battery of claim 1 wherein the test result is related to a dynamic parameter of the battery.

10. The storage battery of claim 1 wherein the dynamic parameter is determined as a function of an electrical load to the battery.

11. The storage battery of claim 1 including a temperature sensor thermally coupled to the battery.

12. The storage battery of claim 11 wherein temperature measurements from the temperature sensor are stored in a memory.

13. The storage battery of claim 11 wherein the battery test result is a function of the measured temperature.

14. The storage battery of claim 1 wherein the output comprises at least one LED.

15. The storage battery of claim 1 including a user operable switch configured to initiate output of a battery test result.

---

16. The storage battery of claim 15 wherein the output battery test result is retrieved from a memory.

17. The storage battery of claim 1 wherein a battery test is performed during periods of non-use of the battery.

-18-

18. The storage battery of claim 1 wherein the battery test is performed during periods of reduced noise in the battery.

19. The storage battery of claim 14 wherein the at least one LED is turned on and off in a sequence to convey information.

20. The storage battery of claim 1 wherein the output comprises an audio output.

21. The storage battery of claim 2 including a switch and wherein information is retrieved from the memory only after receipt of a coded input through the switch.

22. The storage battery of claim 1 wherein the output comprises an IR link.

23. The storage battery of claim 1 wherein the output was a proximity communication technique.

24. The storage battery of claim 1 wherein the output is modulated onto a terminal of the battery.

25. The storage battery of claim 2 wherein information stored in the memory includes information related to a date.

26. The storage battery of claim 1 including a memory which stores information related to a rating of the battery and the battery test result is a function of the stored information.

27. An auxiliary power system, comprising:  
an auxiliary battery;

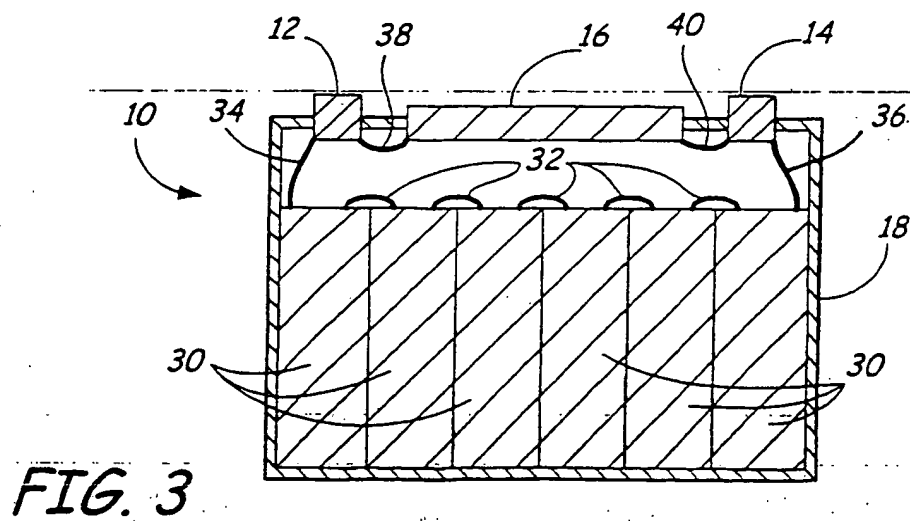
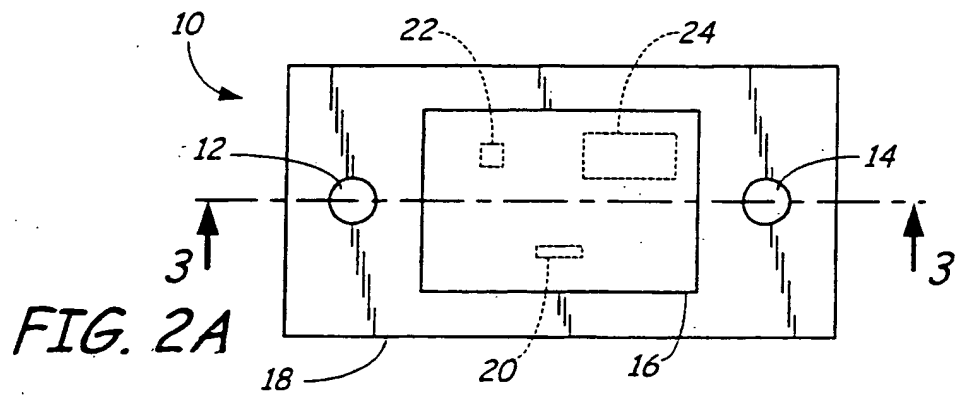
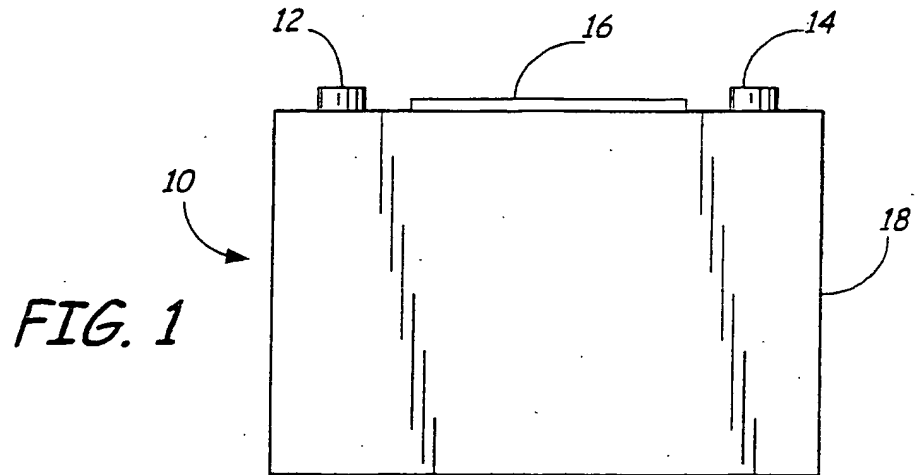
---

a battery test module electrically coupled to the auxiliary battery and configured to perform a battery test on the auxiliary battery and responsively provide a battery test output; and

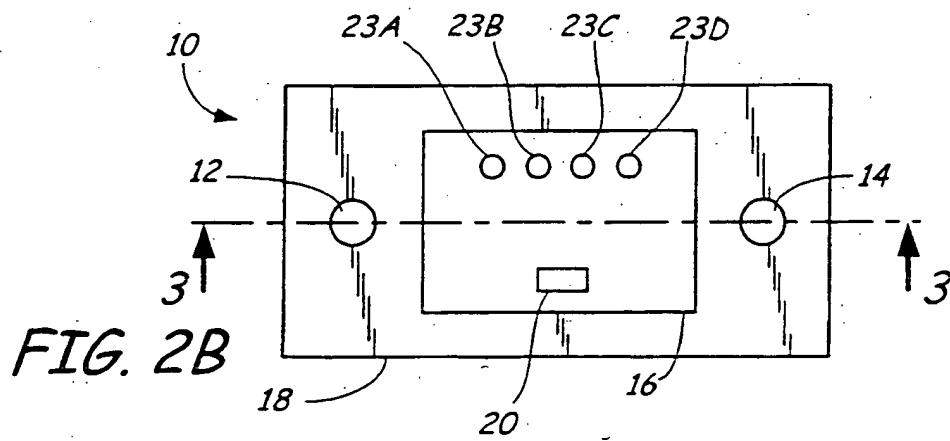
-19-

an output configured to output results of  
the battery test output.

1/5



2/5



3/5

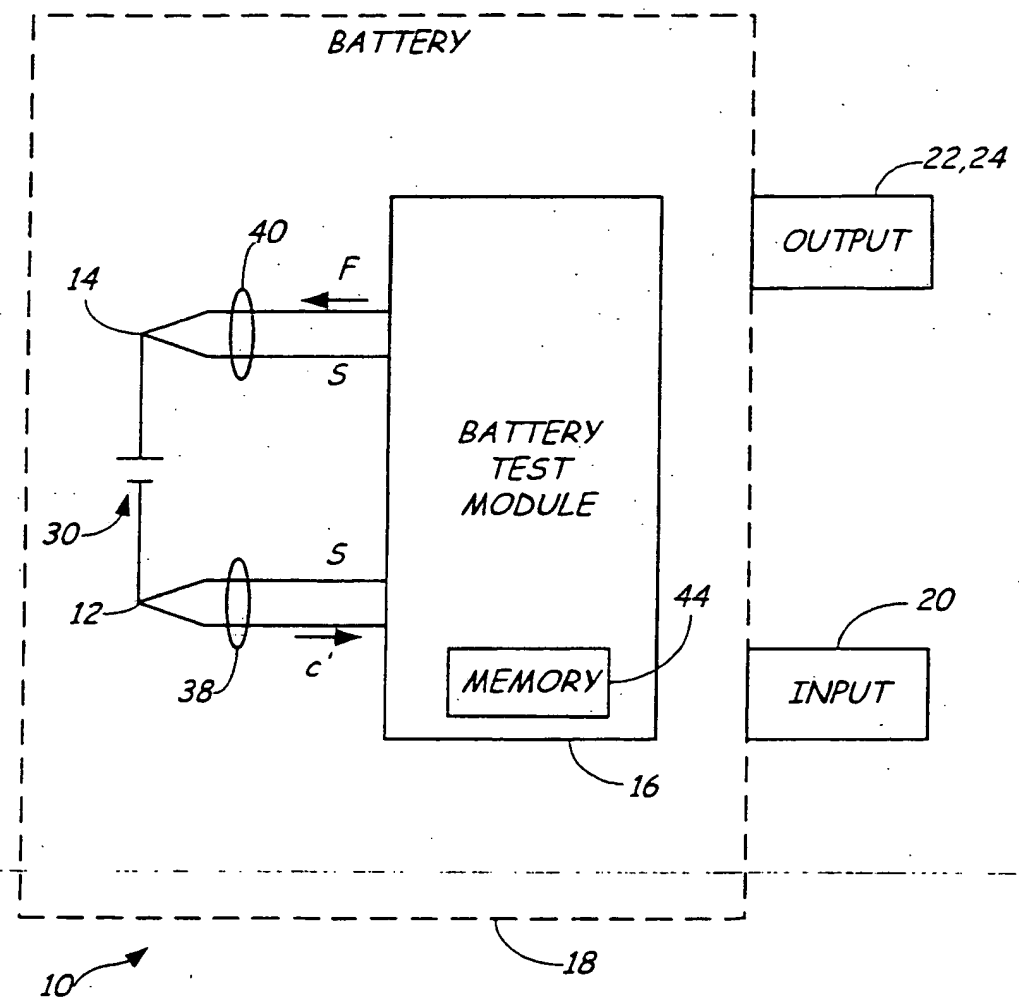


FIG. 4

4/5

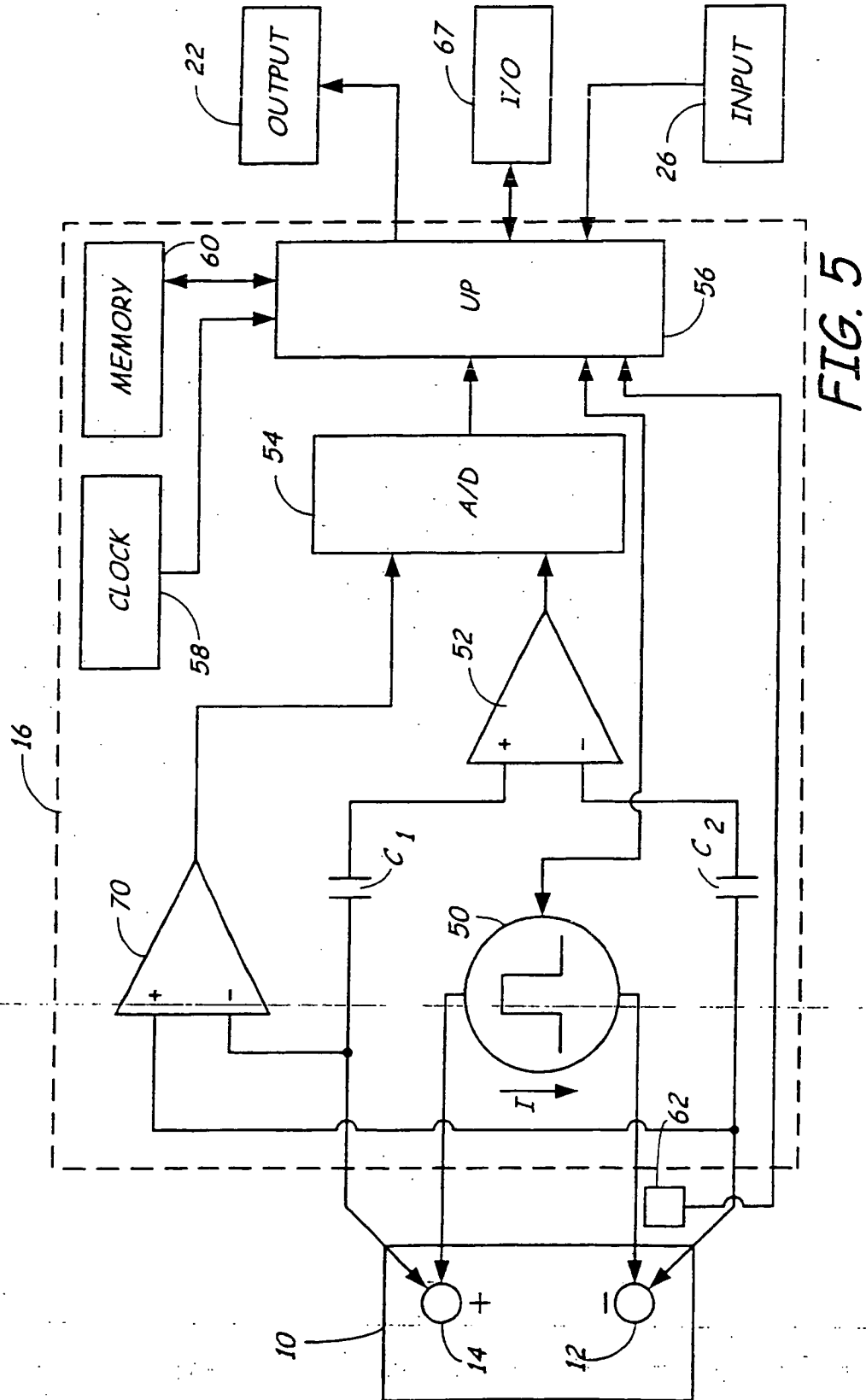
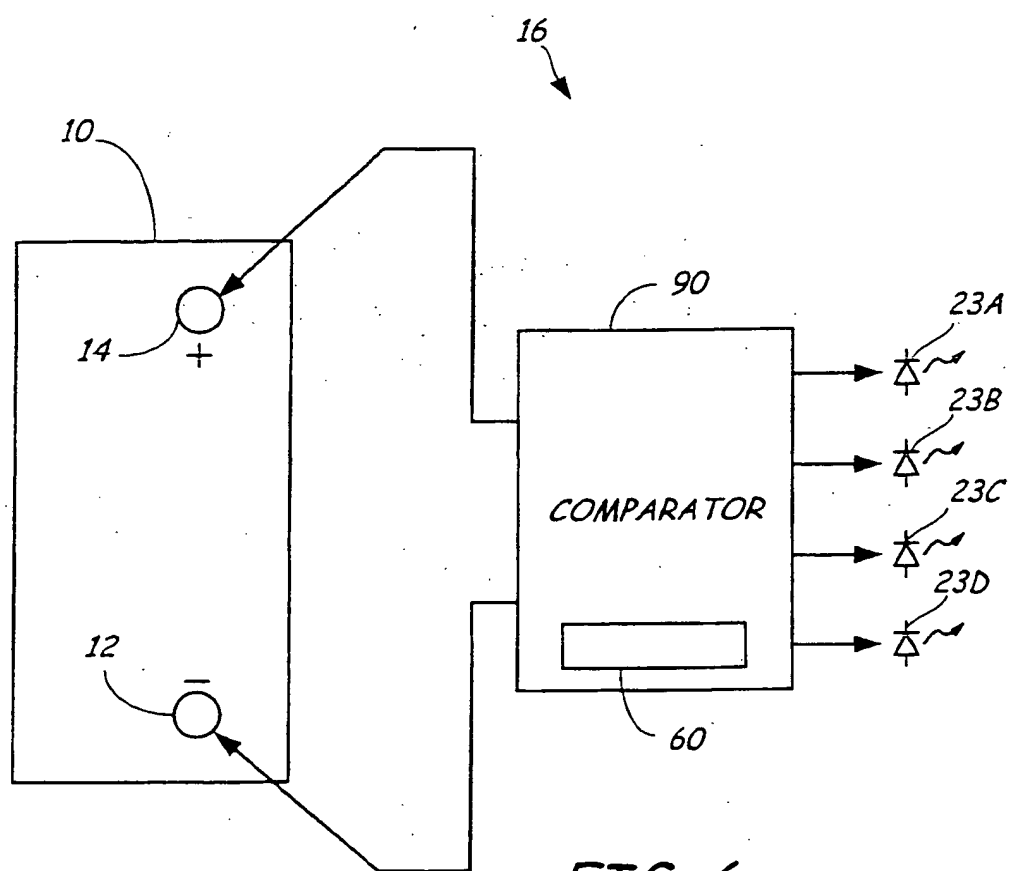


FIG. 5

5/5

**FIG. 6**

## INTERNATIONAL SEARCH REPORT

International application No.  
PCT/US01/04193

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G01N 27/416

US CL : 324/429

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 324/429, 430, 434; 320/120, 132, 134

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched  
NONEElectronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
APS, EPOABS

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6,127,505 B1 (BERTNESS) 09 January 2001 (09.01.2001), see entire document.	1-27
X	US 6,104,167 A (BERTNESS et al.) 15 August 2000 (15.08.2000), see entire document.	1-27
X	US 6,091,245 A (BERTNESS) 18 July 2000 (18.07.2000), see entire document.	1-27
X	US 6,051,976 A (BERTNESS) 18 April 2000 (18.04.2000), see entire document.	1-27



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents:		*T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
*A* document defining the general state of the art which is not considered to be of particular relevance		*X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
*E* earlier document published on or after the international filing date		*Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
*L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)		*G* document member of the same patent family
*O* document referring to an oral disclosure, use, exhibition or other means		
*P* document published prior to the international filing date but later than the priority date claimed		

Date of the actual completion of the international search

03 APRIL 2001

Date of mailing of the international search report

23 APR 2001

Name and mailing address of the ISA/US  
Commissioner of Patents and Trademarks  
Box PCT  
Washington, D.C. 20231

Facsimile No. (703) 305-3230

Authorized officer

GREGORY J. TOATLEY, JR.

Telephone No. (703) 308-1782